



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): **Shaw (et al.)**
Serial No.: **09/051,263**
Filed: **August 7, 1998**

Attorney Docket: **0081-012**
Examiner: **Li, Aimee J.**
Group Art Unit: **2183**

Title: **Shared Architectural Resources of a RISC Microprocessor (as amended)**

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Customer No.: **40972**

INFORMATION DISCLOSURE STATEMENT

Pursuant to 37 C.F.R. §1.56, §1.97, and §1.98, Applicants bring the following documents to the Examiner's attention in the above-referenced application:

1. C. Whitby-Strevens, "The Transputer", The 12th Annual International Symposium on Computer Architecture, Conference Proceedings, June 17-19, 1985, pp. 292-300.;
2. D.W. Best et al., "An Advanced-Architecture CMOS/SOS Microprocessor", IEEE Micro., Vol. 2, No. 3, August 1982, pp. 11-25.; and
3. Intel 80386 Programmer's Reference Manual, 1986, pp. 85-90.

A PTO form 1449 listing these documents is enclosed.

The relevance of the attached references is that this is the closest art of which Applicant is aware.

Citation of the above documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

06/29/2007 RMEBRAHT 00000065 09051263
02 FC:1806 180.00 OP

This information disclosure statement is filed after the mailing of an office action on the merits, but before the mailing of a final action. Therefore, pursuant to 37 CFR §1.97(c), the fee set forth in 37 CFR §1.17(p) is enclosed.

Respectfully Submitted,

Date: 6/27/07

Larry E. Henneman, Jr.
Larry E. Henneman, Jr., Reg. No. 41,063
Henneman & Associates, PLC
714 West Michigan Avenue
Three Rivers, MI 49093

CERTIFICATE OF MAILING (37 CFR 1.8(A))

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA, 22313-1450

Date: 6/27/07

Larry E. Henneman, Jr.
Larry E. Henneman, Jr.

FORM PTO-1449	ATTY. DOCKET NO. 0081-012	SERIAL NO. 09/051,263
APPLICANT Shaw (et al)		
FILING DATE August 7, 1998	GROUP 2183	



REFERENCE DESIGNATION U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

AA	C. Whitby-Stevens, "The Transputer", The 12 th Annual International Symposium on Computer Architecture, Conference Proceedings, June 17-19, 1985, pp. 292-300.
AB	D.W. Best et al., "An Advanced-Architecture CMOS/SOS Microprocessor", IEEE Micro., Vol. 2, No. 3, August 1982, pp. 11-25.
AC	Intel 80386 Programmer's Reference Manual, 1986, pp. 85-90.

EXAMINER

DATE CONSIDERED